Class Day	Theory Topics
1st	Introduction of Computer System Architecture
2nd	Basic structure of computer hardware
3rd	Computer Components
4th	Functional units
5th	—
1st	Performance Measures
2nd	Memory addressing & operations
3rd	Fundamentals of instructions
4th	instruction of Sequencing
5th	——
1st	Operands
2nd	Op codes
3rd	Instruction Formats
4th	Addressing modes
5th	——

Class Day	Theory Topics
1st	Discussion questions of Module 1 and 2
2nd	Register Files
3rd	Complete instruction execution
4th	Steps of instruction
5th	
1st	Hardware control
2nd	Micro program control
3rd	Fetching concept
4th	Decode
5th	
1st	Execution steps
2nd	Procedure of processor system
3rd	Class test
4th	Introduction of Memory System
5th	

1st	Characteristics of Memory
2nd	Memory hierarchy
3rd	RAM and ROM organization
4th	Interleaved Memory
5th	
1st	Cache Memory
2nd	Virtual memory
3rd	Primary Memory or Main Memory
4th	Types of ROM
5th	
1st	Assesment Test
2nd	Basic concepts of input & output devices
3rd	input - output interface
4th	Modes of data transfer
5th	

Class Day	Theory Topics
1st	Difference between input & output devices
2nd	Programmed input output transfer
3rd	Interrupt driven in input & output devices
4th	DMA (Direct Memory Access)
5th	
1st	Input - output processor
2nd	Basic and Brief discussion of input - output system
3rd	Practice test with some question
4th	Input - output interface & Bus architecture
5th	
1st	Bus & system Bus, Types of system Bus
2nd	Briefly discussion in types of system Bus
3rd	Data bus, Address bus, control bus
4th	Structure of Bus
5th	

1st	Basic parameters of BUS design
2nd	SCSI
3rd	USB
4th	Test
5th	
1st	Discussion on question & answers on BUS architecture
2nd	Introduction in parallel processing
3rd	Key points of processing
4th	parallel processing
5th	
1st	Linear pipeline
2nd	Multiprocessor
3rd	Flynn's classification
4th	Overall discussion of Computer system Architecture
5th	