

CSE	Semester: 3rd	Name of the Teaching Faculty Snehalata Rout	
Computer System Architecture	No of Days/Week Class Allotted: 04	Semester From date: _____ To date _____	No. of Weeks: _____

Class Day	Theory Topics
1st	Introduction of Computer System Architecture
2nd	Basic structure of Computer Hardware
3rd	Computer Components
4th	Functional units
5th	—
1st	Performance Measures
2nd	Memory - addressing & operations
3rd	Fundamentals of instructions
4th	Instruction of Sequencing
5th	—
1st	Operands
2nd	OP codes
3rd	Instruction Formats
4th	Addressing modes
5th	—

Semester:	Name of the Teaching Faculty	
No of Days/Week Class Allotted: _____	Semester From date: _____ To date _____	No. of Weeks:

Class Day	Theory Topics
1st	Discussion questions of module 1 and 2
2nd	Register Files
3rd	Complete instruction execution
4th	Read steps of instruction
5th	
1st	Hardware control
2nd	Micro program control
3rd	Fetching concept
4th	Decode
5th	
1st	Execution steps
2nd	Procedure of processor system
3rd	class test
4th	introduction of memory system
5th	

1st	Characteristics of memory
2nd	Memory hierarchy
3rd	RAM and ROM organization
4th	Interleaved memory
5th	
1st	Cache memory
2nd	Virtual memory
3rd	Primary memory or Main memory
4th	Types of ROM
5th	
1st	Assessment Test
2nd	Basic concepts of input & output devices
3rd	Input-output interface
4th	Modes of data transfer
5th	

ARYAN SCHOOL OF ENGINEERING & TECHNOLOGY, BHUBANESWAR

		Semester:	Name of the Teaching Faculty	
		No of Days/Week Class Allotted: _____	Semester From date: _____ To date _____	No. of Weeks:
Class Day	Theory Topics			
1st	Difference Between input & output devices			
2nd	programmed input output transfer			
3rd	interrupt driven in input & output devices			
4th	DMA (Direct Memory Access)			
5th				
1st	input-output processor			
2nd	Basic and Brief discussion of input-output system			
3rd	Practice test with some questions			
4th	input-output interface & BUS architecture			
5th				
1st	BUS & system Bus, Types of system Bus			
2nd	Briefly discussion on types of system Bus			
3rd	Data bus, Address bus, Control bus			
4th	structure of Bus			
5th				

27.01.2022 TO 31.01.2022	1st	Basic parameters of BUS design
	2nd	SCSE
	3rd	USB
	4th	Test
	5th	
30.01.2022 TO 01.02.2022	1st	Discussion in question & answer in BUS architecture
	2nd	Introduction in parallel processing
	3rd	Key points of processing
	4th	Parallel processing
	5th	
30.01.2022	1st	Linear pipeline
	2nd	Multiprocessor
	3rd	Flynn's classification
	4th	Overall discussion of Computer system architecture
	5th	